

FIG. 1

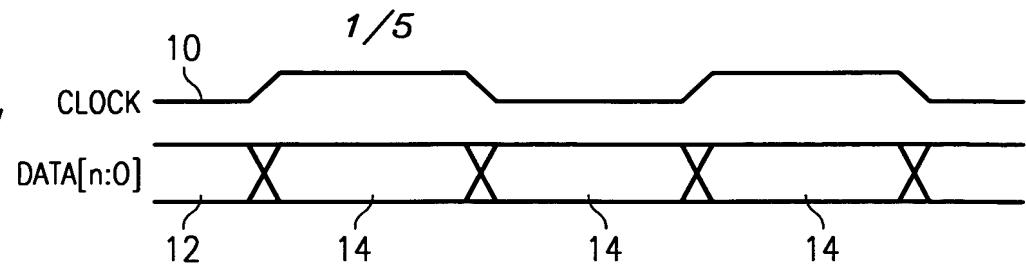


FIG. 2

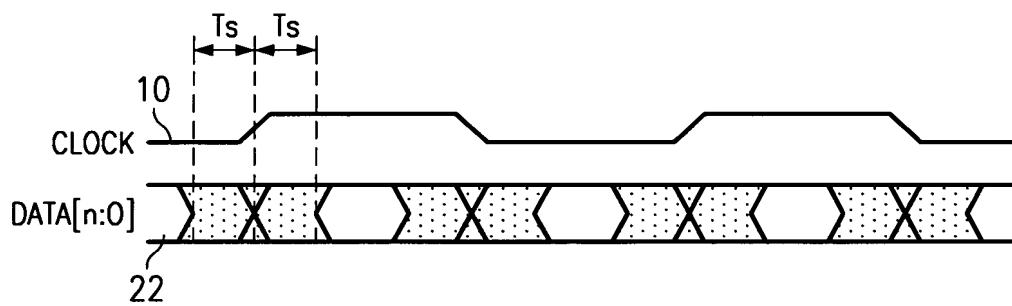


FIG. 4A

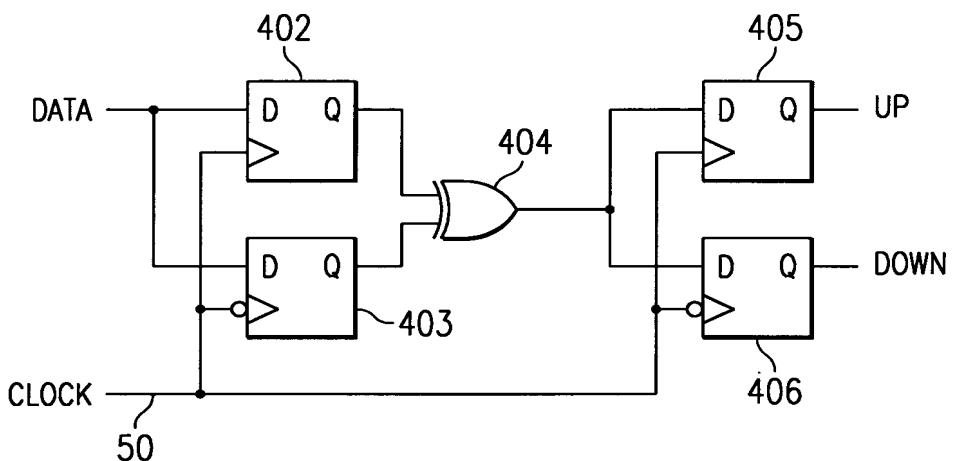


FIG. 4B

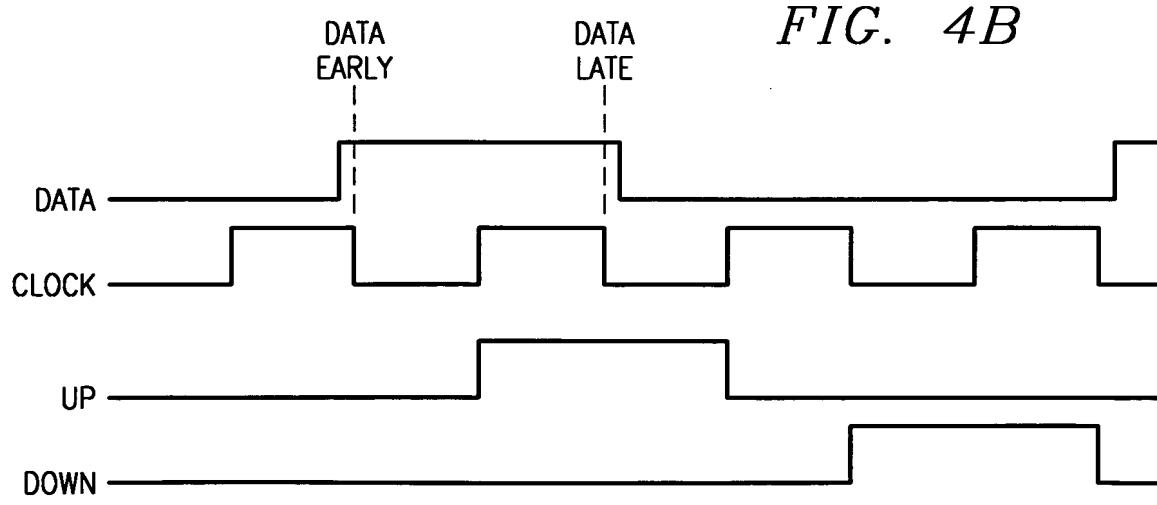


FIG. 3

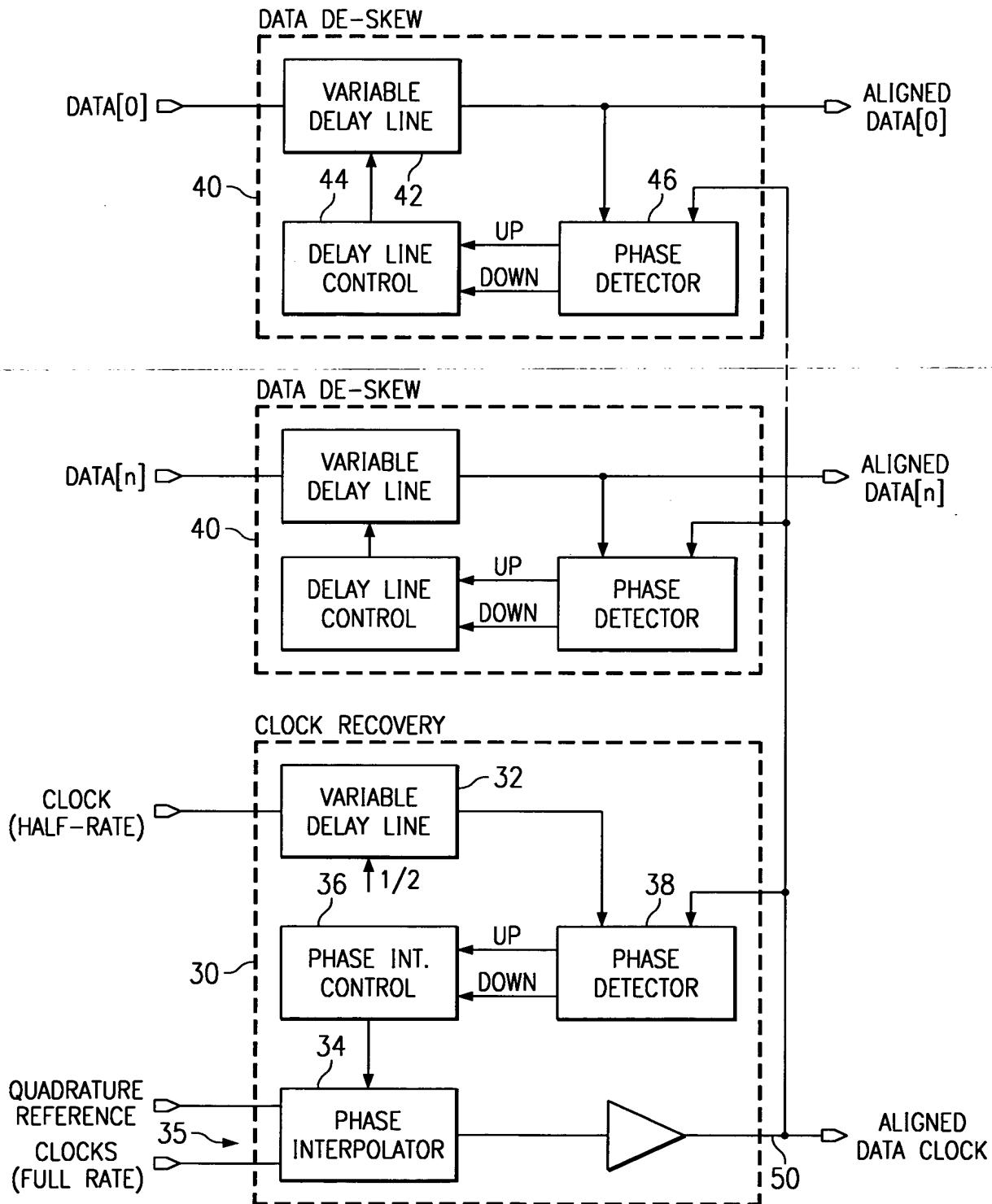


FIG. 5

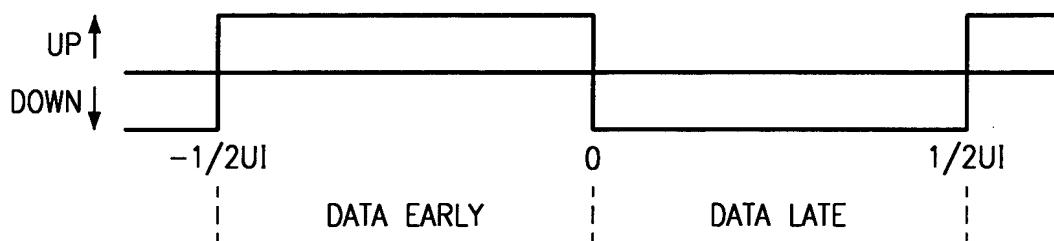


FIG. 6

DATA DELAY ADJUSTMENT RANGE

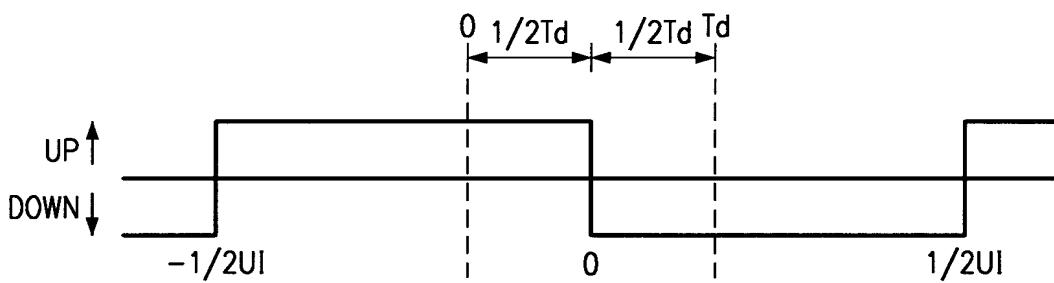


FIG. 7

DATA DELAY ADJUSTMENT RANGE

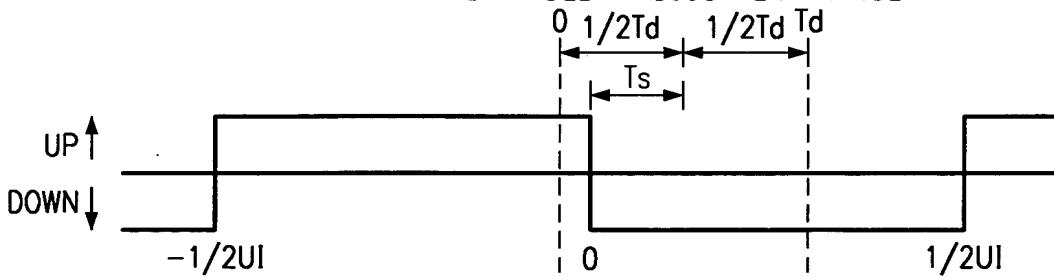


FIG. 8

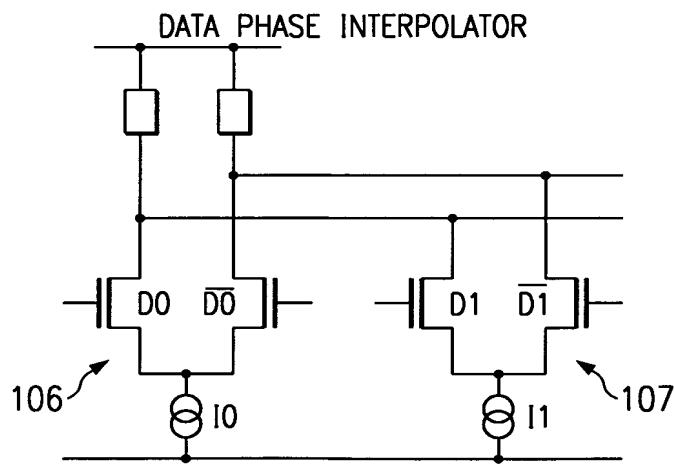
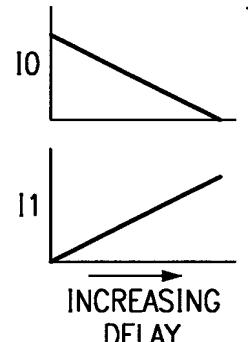
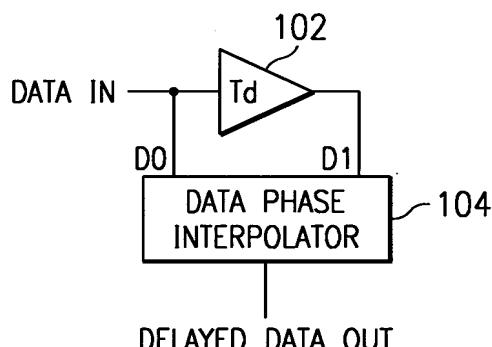
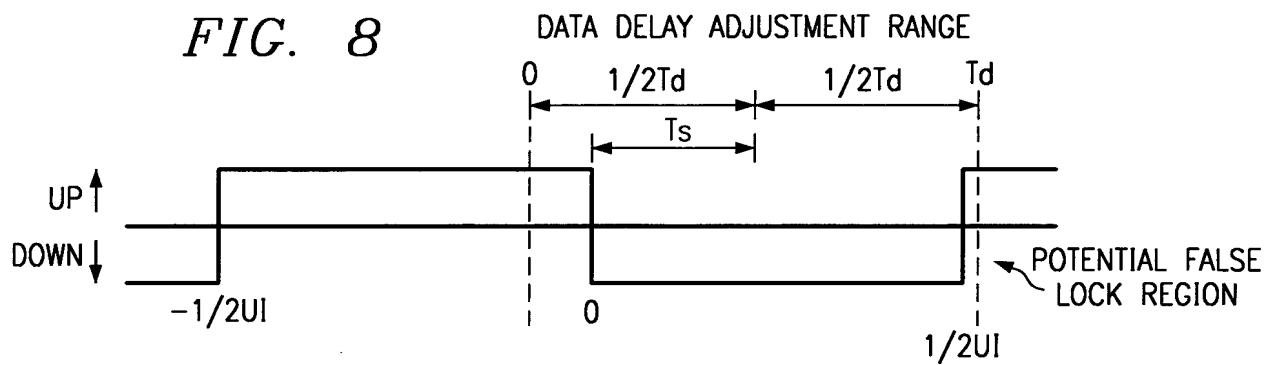


FIG. 10

